

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Sehat Sutardja et al Art Unit : Unknown
Serial No. : Unknown Examiner : Unknown
Filed : November 12, 2003
Title : Output Regulator System and Method

Commissioner for Patents
Washington, D.C. 20231


INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449, copies of which are enclosed.

This statement is being filed with the application. Please apply any charges or credits to Deposit Account No. 50-1236.

Respectfully submitted,

Date: 11-12-03



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	<p>APPLICANT</p> <p>Sehat Sutardja et al</p>	
	<p>FILING DATE</p> <p>November 12 , 2003</p>	<p>GROUP</p> <p>Unknown</p>

U.S. PATENT DOCUMENTS						
Ref. desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date
1.		6,268,716B1	7/31/2001	Burstein et al		
2.		6,329,801B1	12/11/2001	Zuniga et al		
3.		6,100,676	8/8/2000	Burstein et al		
4.		60/395,115	7/10/2002	Sutardja et al		
5.		60/395,697	7/12/2002	Sutardja et al		
6.		10/460,825	6/12/2003	Sutardja et al		

FOREIGN PATENT DOCUMENTS							
Ref. Desig.	Examiner's Initials	Document Number	Date	Country	Class/ Subclass	Yes	No
1.							

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)		
Ref. Desig.	Examiner's Initials	Document
1.		Current Sensing Solutions for Power Supply Designers, by Bob Mammano, Unitrode Power Supply Design Seminar, 1997, pgs. 1-34
2.		HIP6301 Data Sheet, Intersil, March 2000, pgs. 1-16
3.		LM2639 Data Sheet, National Semiconductor, February 2000, pgs. 1-8
4.		TPS5211 Data Sheet, Texas Instruments, September 1999, pgs. 1-32
5.		FAN5094 Data Sheet, Fairchild, 1/10/2002, pgs. 1-22
6.		Review of On-Board Low Voltage/High Current DC-DC Converters for the New Generation of Microprocessors, by Abu-Qahouq et al,
7.		

Examiner:	Date Considered:
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